

S/N 10/643,585

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Steven L. Scott	Examiner:	Sheng Jen Tsai
Serial No.:	10/643,585	Group Art Unit:	2186
Filed:	August 18, 2003	Docket No.:	1376.700US1
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER		

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Office Action mailed on October 23, 2006. Please amend the above-identified patent application as follows.